

DIGITAL FILTER

Patent Number: JP6104694
Publication date: 1994-04-15
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Requested Patent: ☐ JP6104694
Application Number: JP19920277809 19920922
Priority Number(s):
IPC Classification: H03H17/02 ; H03H17/00
EC Classification:
Equivalents:

H6-104694

Abstract

PURPOSE: To reduce the circuit scale of the digital filter by forming the circuit for the digital filter without use of an arithmetic operation circuit.

CONSTITUTION: Plural digital filters 2a-2i for binary input signals whose number corresponds to number of figures of the input signals are provided. Each digital filter is connected corresponding to each figure of the input signal. Furthermore, an output of each digital filter is weighted by weighting circuits 7a-7i corresponding to the figure position and the result is added by adder circuits 8a-8h. Output data from the adder circuit 8h are extracted from an output terminal 9.

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